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(71) Applicant (for all designated States except US): **KONINKLIJKE PHILIPS ELECTRONICS N.V.** [NL/NL];
Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventors; and

(75) Inventors/Applicants (for US only): **KNAPP, Alan, G.** [GB/GB]; c/o Philips Intellectual Property & Standards, Cross Oak Lane, Redhill, Surrey RH1 5HA (GB).

DEANE, Steven, C. [GB/GB]; c/o Philips Intellectual Property & Standards, Cross Oak Lane, Redhill, Surrey RH1 5HA (GB).

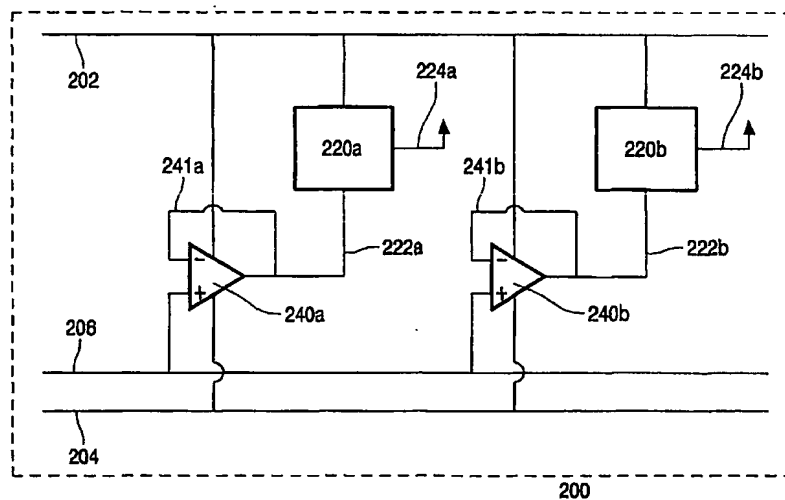
(74) Agent: **WHITE, Andrew, G.**; Philips Intellectual Property & Standards, Cross Oak Lane, Redhill, Surrey RH1 5HA (GB).

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(54) Title: INTEGRATED CIRCUIT ARRANGEMENT, INTEGRATED CIRCUIT, MATRIX ARRAY OF CIRCUITS AND ELECTRONIC DEVICE



(57) **Abstract:** An IC arrangement (200) has a plurality of IC modules (220a, 220b), the individual IC modules (220a, 220b) being coupled between a first power line (202) and a second power line (204) via a voltage generator (240a, 240b). The voltage generators (240a, 240b) are powered via the first power line (202) and the second power line (204) and are arranged to regenerate a reference voltage on a reference power line (106) for providing the IC modules (220a, 220b) with the regenerated voltage on respective internal power lines (222a, 222b). A feedback loop (242a, 242b) from the internal power lines (222a, 222b) to the voltage generator (240a, 240b) ensures that the voltage on the internal power lines (222a, 222b) remains substantially constant, even if substantial current fluctuations on the first power line (202), the second power line (204) or the internal power line (222a, 222b) occur. The IC arrangement (200) is particularly suitable as a driver circuit for a matrix array device.

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